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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/711,937	10/14/2004	Tainder Yeh	13474-US-PA	5936

31561 7590 02/07/2008

JIANQ CHYUN INTELLECTUAL PROPERTY OFFICE
7 FLOOR-1, NO. 100
ROOSEVELT ROAD, SECTION 2
TAIPEI, 100
TAIWAN

EXAMINER	
YUN, EUGENE	
ART UNIT	**PAPER NUMBER**
2618	

NOTIFICATION DATE	DELIVERY MODE
02/07/2008	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

USA@JCIPGROUP.COM.TW

Office Action Summary	Application No.	Applicant(s)	
	10/711,937	YEH, TAINDER	
	Examiner	Art Unit	
	Eugene Yun	2618	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *09 November 2007*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-14* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-14* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *14 October 2004* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-14 are rejected under 35 U.S.C. 102(b) as being anticipated by Tran et al. (US 5,541,924).

Referring to Claim 1, Tran teaches a channel sharing method, comprising: providing a plurality of channels, wherein each of the channels comprises a time interval of signal transmission (see col. 2, lines 45-55);

providing a time slot, wherein a width of the time slot is X times of a maximum value of all the time intervals, and X is a positive number (see col. 6, lines 28-40); each of the channels is generated by a permutation of at least one repeat time, and the repeat time is M times of the width of the time slot, wherein M is an integer larger than O (see col. 6, lines 46-56), and a first time slot of the repeat time comprises a signal, and a maximum time span of the signals in each of the channels is the time interval of each of the channels (see col. 6, lines 56-64); and arranging all the channels so that at least one of the signals in each of the channels is not collided with the signals of the other channels in a worst time delay (see col. 4, lines 47-60).

Referring to Claim 6, Tran teaches a channel sharing device, comprising:

a plurality of transmission devices (fig. 1), wherein each of the transmission devices comprises a transmitter and an encoder, wherein the encoder generates a channel with a signal, and the transmitter transmits a wireless signal; and a plurality of receiving devices, wherein each of the receiving devices comprises a receiver and a decoder, wherein the receiver receives the wireless signal, the decoder decodes the wireless signal to obtain the signal (see col. 9, lines 9-25), wherein each of the channels comprises:

a time interval and a time slot, wherein a width of the time slot is X times of a maximum value of the time intervals of the channels, and X is a positive number (see col. 6, lines 28-40); each of the channels is generated by a permutation of at least one repeat time, and the repeat time is M times of the width of the time slot; wherein M is an integer larger than O (see col. 6, lines 46-56), and a first time slot of the repeat time comprises the signal, and a maximum time span of the signals in each of the channel is the time interval of each of the channels (see col. 6, lines 56-64); all the channels are arranged so that at least one of the signals in each of the channels is not collided with the signals of the other channels in a worst time delay (see col. 4, lines 47-60).

Referring to Claims 2 and 12, Tran also teaches the width of the slot twice of the maximum value of all the time intervals (see col., 6, lines 28-35).

Referring to Claims 3 and 13, Tran also teaches at least one of the channels comprising two repeat times with different lengths (see col. 8, lines 35-50).

Referring to Claim 4, Tran also teaches the step of arranging the channels comprising checking a preset table (see col. 14, lines 42-45).

Referring to Claim 5, Tran also teaches computation by a program of a software (see col. 11, lines 29-31).

Referring to Claim 7, Tran also teaches each of the transmission devices corresponding to at least one of the receiving devices (see fig. 1).

Referring to Claim 8, Tran also teaches the encoder comprising a first clock generator and first channel generator, wherein the first clock generator generates a clock signal, and the first channel generator generates the channel comprising the signal (see col. 9, lines 9-25).

Referring to Claim 9, Tran also teaches the first channel generator comprising a preset table, a program or a software (see col. 11, lines 29-31).

Referring to Claim 10, Tran also teaches the decoder comprising a second clock generator and a second channel generator, wherein the second clock generator generates a clock signal, and the second channel generator decodes the wireless signal to obtain the signal (see col. 9, lines 9-25).

Referring to Claim 11, Tran also teaches second channel generator comprising a preset table, a program of software (see col. 11, lines 29-31).

Referring to Claim 14, Tran also teaches the transmitter or the receiver comprising a radio frequency (RF) generator and an antenna (see RF and antennas in fig. 1).

Response to Arguments

3. Applicant's arguments filed 11/9/2007 have been fully considered but they are not persuasive.

The applicant argued that the Tran reference does not teach " providing a time slot, wherein a width of the time slot is X times of a maximum value of all the time intervals, and X is a positive number; each of the channels is generated by a permutation of at least one repeat time, and the repeat time is M times of the width of the time slot, wherein M is an integer larger than O, and a first time slot of the repeat time comprises a signal, and a maximum time span of the signals in each of the channels is the time interval of each of the channels; and arranging all the channels so that at least one of the signals in each of the channels is not collided with the signals of the other channels in a worst time delay".

Firstly, the applicant's arguments were believed by the examiner to be very vague. The arguments simply told what the Tran reference taught and then concluded that the Trän reference does not teach the above limitations without stating why or what the difference is between the Tran reference and what is taught in the claimed Invention. The examiner cannot be persuaded is he does not know why the Tran reference does not teach the claimed invention according the applicant.

In addition, the examiner states that the integers X and M can equal 1. This would mean that the width of a time slot is the same as a max value of all the time intervals and that there is only one repeat time the same as the width of the time slot. This is believed to be known in the art and taught in the Tran reference.

Conclusion

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eugene Yun whose telephone number is (571) 272-7860. The examiner can normally be reached on 9:00am-6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew D. Anderson can be reached on (571)272-4177. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Eugene Yun
Examiner
Art Unit 2618

EY

MATTHEW ANDERSON
SUPERVISORY PATENT EXAMINER